



Attorney Docket No.: YOR920030306US1

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Patent Application**

Applicant(s): M. Kapur et al.  
Docket No.: YOR920030306US1  
Serial No.: 10/650,222  
Filing Date: August 28, 2003  
Group: To Be Assigned  
Examiner: To Be Assigned

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Signature: William E. Lewis Date: October 6, 2003

Title: Self-Synchronizing Pseudorandom Bit Sequence Checker

TRANSMITTAL OF FORMAL DRAWINGS

Mail Stop PGPUB Drawings  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicants submit herewith six (6) sheets of formal drawings in the above-referenced patent application.

Respectfully submitted,

Date: October 6, 2003

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